

Reinforcement Learning-Based PI Control Strategy for Single-Phase Voltage Source PWM Rectifier

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Abstract: To address issues of lagging dynamic response and complex parameter tuning in traditional double-loop control of single-phase voltage source PWM rectifiers, this research proposes a hierarchical intelligent control strategy integrating reinforcement learning (RL). Firstly, the mathematical model in the d-q rotating coordinate system is established by analyzing the circuit topology of a single-phase VSR. Subsequently, a double-loop control structure comprising a voltage outer loop and a current inner loop is developed: the current inner loop adopts DQ feedforward decoupling control to achieve independent conditions; the voltage outer loop innovatively employs a single-neuron PI controller based on reinforcement learning, which optimizes control parameters in real-time via a deep deterministic policy gradient (DDPG) algorithm, thus forming an adaptive hierarchical control system. Finally, the effectiveness of the strategy is validated through simulation models built on the Matlab/Simulink platform. Simulation results demonstrate superior dynamic performance of the proposed method under load mutation conditions, significantly improving dynamic response quality and steady-state performance.

Keywords: PWM rectifier, PI control, current DQ decoupling, neuron, deep deterministic policy gradient (DDPG)

1. Introduction

Pulse-width modulation (PWM) rectifiers are classified into voltage source rectifiers (VSR) and current source rectifiers (CSR). Single-phase voltage source PWM rectifiers [1], characterized by reversible energy flow, adjustable output voltage, low grid-side current harmonic content, and stable unit power factor operation, have emerged as a research hotspot in the rectifier domain, widely applied in various industrial equipment such as UPS, communication power supplies, and electric vehicle charging systems. Research on single-phase VSR control strategies is critically important for effectively implementing power factor correction (PFC) and reducing total harmonic distortion (THD) of grid-side current.

At present, numerous studies on current control techniques for single-phase VSR have been conducted domestically and internationally, primarily encompassing hysteresis control [2], PR control [3], deadbeat control [4], and PI control. The hysteresis control structure is simple and robust, but the switching frequency varies with hysteresis width, causing high current harmonics and larger errors. PR control of the current inner loop effectively reduces grid-side current THD but exhibits limited stability margins sensitive to grid voltage frequency variations. The deadbeat control method provides rapid dynamic response and high tracking accuracy; however, it involves substantial

computational complexity and poor robustness. PI control achieves rapid response and eliminates DC offset. Enhanced PI controllers further improve system stability and robustness, boasting simple structures and low implementation costs.

Currently, single-phase VSR extensively utilizes a dual-loop voltage-current control strategy, primarily regulating DC side voltage and grid-side current [5-6]. As an active front-end, single-phase VSR requires controlling DC-side output voltage. In the current loop, employing PI controllers necessitates obtaining sinusoidal AC voltage and current signals in the stationary $\alpha\beta$ coordinate system through phase-locked techniques [7-8], then converting these signals into DC quantities via Park transformation in the synchronous rotating d-q coordinate system to execute feedforward decoupling control [9-11], thus achieving zero steady-state error tracking of AC current. However, the DQ current decoupling control involves multiple PI loops, impacting system dynamic response speed to some extent. It becomes challenging to attain high control performance and effectively suppress DC voltage fluctuations under strong disturbances or pronounced nonlinear characteristics of the controlled object [12-13]. Improved controllers, such as single-neuron PI controllers, calculate control law increments based on input weights, input magnitudes, and activation functions [14-15]. However, learning rates and transfer coefficients still significantly influence the dynamic response of the entire system. The control strategy proposed in reference [16] maintains converter stability under various disturbances but complicates the control structure and requires extensive computation.

Based on DQ current decoupling control of single-phase voltage source PWM rectifiers, this research first clarifies the circuit topology and constructs a mathematical model in the d-q rotating coordinate system. Subsequently, a single-neuron PI controller is designed for the voltage outer loop, and feedforward decoupling control is applied in the current inner loop. Lastly, reinforcement learning constitutes the upper layer, optimizing the initial parameters, the neuron's learning rate η , and the transfer coefficient K of the underlying single-neuron PI controller. Through comparative simulations involving sudden partial load removal and constant power load variations, the new reinforcement learning-based controller is evaluated against traditional PI and single-neuron PI controllers. Results validate the superior dynamic performance of the RL-integrated single-neuron PI controller in rapidly tracking DC bus voltage.

2. Working principle of single-phase VSR-PWM

2.1. PWM rectifier topology and mathematical model

Fig. 1 illustrates the primary circuit topology of a single-phase VSR. In this figure, u_s denotes the grid-side input voltage; i_s is the grid-side input current; L represents the AC-side filter inductance; R_s is the equivalent line impedance; u_{ab} indicates the AC-side input voltage of the full-bridge rectifier; C is the DC-side filter capacitor; R_L stands for the load resistance; and $S_1 \sim S_4$ represent the fully controlled switches MOSFETs of the rectifier.

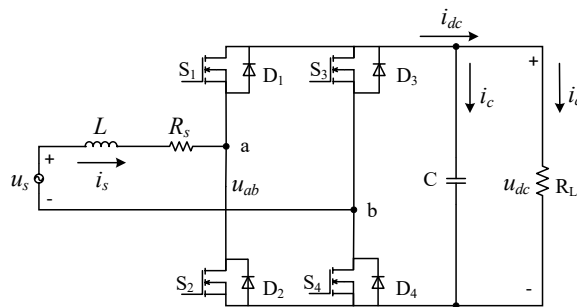


Figure 1: Topology of single-phase PWM rectifier

According to KVL, the single-phase VSR satisfies the following equation:

$$u_s = L \frac{di_s}{dt} + R_s + u_{ab} \quad (1)$$

To implement decoupled control for the current inner loop of the single-phase VSR, it is necessary to construct a virtual quantity lagging the grid voltage and current by 90° , subsequently employing Park transformation to derive the mathematical model in the d-q rotating coordinate system. This paper utilizes a second-order generalized integrator-based single-phase phase-locked loop (SOGI-PLL) to provide the angular frequency ω required by the Park transformation. Essentially, SOGI-PLL comprises a quadrature pair consisting of a second-order band-pass filter and a second-order low-pass filter, offering specific filtering effects. Its structural block diagram is shown in ref. [8]. Using SOGI-PLL, virtual orthogonal components $u_{s\alpha}$, $u_{s\beta}$, $i_{s\alpha}$, $i_{s\beta}$ of the grid voltage u_s and grid current i_s in the stationary $\alpha\beta$ and (3) from ref. [9] and equ. (7) and (8) from ref. [10], the mathematical model of the single-phase VSR in the d-q rotating coordinate system is acquired. Thus, voltage components u_{sd} , u_{sq} in the d-q rotating coordinate system are given as follows:

$$\begin{cases} u_{sd} = L \frac{di_{sd}}{dt} + \omega L i_{sq} + u_{abd} \\ u_{sq} = L \frac{di_{sq}}{dt} - \omega L i_{sd} + u_{abq} \end{cases} \quad (2)$$

2.2. Voltage and current dual-loop control strategy

The control scheme of the single-phase VSR comprises two parts: the voltage outer-loop PI control and the current inner-loop DQ decoupling control. The overall control structure is illustrated in Fig. 2.

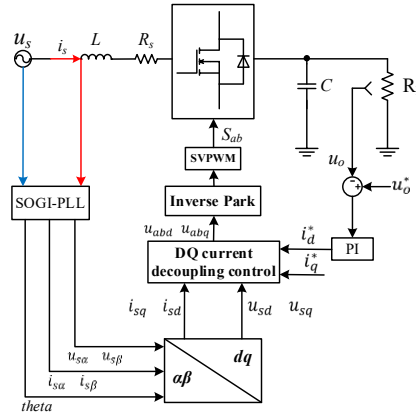


Figure 2: Block diagram of system control

When operating as an active front-end, the main objectives of the single-phase VSR control are maintaining constant DC-side voltage, achieving sinusoidal grid-side current, and ensuring operation at unity power factor. To maintain a stable DC-side voltage, the reference voltage u_o^* on the DC side is compared with the actual output voltage u_o . Their difference, after passing through a PI controller and limiter, serves as the reference current i_d^* for the grid-side current on the d-axis in the inner-loop current control. Thus:

$$i_d^* = \left(k_p + \frac{k_i}{s}\right) (u_o^* - u_o) \quad (3)$$

From the Eq. (2) of the single-phase VSR in the d - q rotating coordinate system, the voltage components u_{abd} , u_{abq} of rectifier input voltage u_{ab} can be rearranged as follows:

$$\begin{cases} u_{abd} = u_{sd} - L \frac{di_{sd}}{dt} - \omega L i_{sq} \\ u_{abq} = u_{sq} - L \frac{di_{sq}}{dt} + \omega L i_{sd} \end{cases} \quad (4)$$

Defining $L \frac{di_s}{dt} = L \frac{i_s^* - i_s}{T_s}$, where T_s is the switching period and i_s^* is the reference input current, the above equation simplifies to:

$$\begin{cases} u_{abd} = u_{sd} - L \frac{i_{sd}^* - i_{sd}}{T_s} - \omega L i_{sq} \\ u_{abq} = u_{sq} - L \frac{i_{sq}^* - i_{sq}}{T_s} + \omega L i_{sd} \end{cases} \quad (5)$$

Based on Eq. (5), the control structure for DQ current decoupling is obtained, as illustrated in Fig. 3.

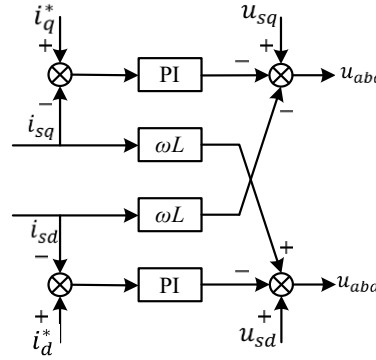


Figure 3: Diagram of DQ decoupling current control strategy

3. Design of single-neuron PI controller based on RL

3.1. Fundamental principles of RL

RL is a special class of algorithms in machine learning based on the Markov Decision Process (MDP). Its core idea involves an agent interacting with the environment to learn optimal strategies. The environment reinforces the agent's correct actions through rewards, prompting continuous improvement in behavior to maximize cumulative returns. The interaction workflow of reinforcement learning is depicted in Fig. 4, where the Agent represents the entity undergoing training, and the Environment is the entity with which the Agent interacts. The interaction occurs through actions, states, and rewards: at time step t , the Agent observes the current state (S_t), executes an action (A_t), subsequently observes the new state (S_{t+1}), and finally receives a reward (R_t). In brief, reinforcement learning aims to train the Agent to better interact with the Environment and thus achieve higher rewards.

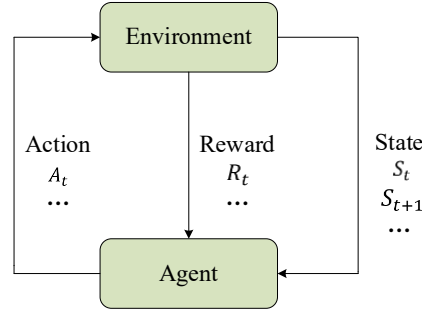


Figure 4: Markov decision process

3.2. Mathematical model of the single-neuron PI controller

This paper focuses on analyzing a positional single-neuron PI controller. The input weight coefficients are defined as $w_1 = K_p$, $w_2 = K_i$, corresponding to the input values:

$$\begin{cases} x_1 = error(k) \\ x_2 = \sum_{z=1,2,\dots,k}^n error(z) \end{cases} \quad (6)$$

Then, the neuron input is:

$$Net_{input} = \sum_{i=1}^2 \omega_i' x_i \quad (7)$$

Here, ω_i' represents normalized input weights, and x_i denotes input signals. The input weights ω_i' are normalized as:

$$\omega_i' = \frac{\omega_i}{\sum_{i=1}^2 |\omega_i|} \quad (8)$$

The positional activation function is $f_k(x_k) = Kx_k$. Therefore, the positional single-neuron PI control law is given by:

$$u(k) = K \sum_{i=1}^2 \omega_i' (k) x_i(k) \quad (9)$$

In this expression, K is the neuron transfer coefficient, $\omega_i' (k)$ is the normalized weight, and $x_i(k)$ is the corresponding input signal. The single-neuron PI controller achieves adaptive and self-organizing capabilities by adjusting weight coefficients. The adjustment of weight coefficients employs a supervised Hebbian learning rule, related to the neurons input, output, and output deviation as follows:

$$\begin{cases} \omega_i(k+1) = (1-c)\omega_i(k) + \eta r_i(k) \\ r_i(k) = z(k)u(k)x_i(k) \end{cases} \quad (10)$$

Here, $z(k)$ is the output error signal; η represents the learning rate, $\eta > 0$; and c is a constant, $c > 0$. After normalization, the above learning algorithm becomes:

$$\begin{cases} \omega_1(k+1) = \omega_1(k) + \eta_p z(k)u(k)x_1(k) \\ \omega_2(k+1) = \omega_2(k) + \eta_i z(k)u(k)x_2(k) \end{cases} \quad (11)$$

Different learning rates η_p , η_i for proportional (P) and integral (I) parts can be adopted to independently adjust their respective weight coefficients according to needs.

3.3. Training environment design for hyperparameter tuning

Although the single-neuron PI controller exhibits excellent dynamic response capability, it relies heavily on empirically determined hyperparameters, including the learning rate η and transfer coefficient K . Therefore, this paper uses RL at the upper level to optimize these hyperparameters, while employing the single-neuron PI controller at the lower level. Without affecting the single-neuron PI controller's ability to adjust parameters K_p, K_i hierarchical closed-loop controls can operate at different frequencies.

First, the state definition is established as:

$$s_t = \left[e(t), \int_0^t e(\tau) d\tau, \frac{de}{dt}, \frac{d^2e}{dt^2} \right]^T \quad (12)$$

Besides error $e(t)$ and integral error $\int edt$, historical information such as error variation rates $\frac{de_t}{dt}$ can enhance learning efficiency. Higher-order derivatives contain richer state information, especially when the controlled object exhibits high-frequency oscillations or nonlinearities.

Next, the reward function (Reward) is defined. The fundamental reward item, the tracking error, encourages error minimization. After introducing integral penalties, it becomes:

$$R_{\text{error}} = -[w_e \cdot e(t)^2 + w_i (\int ed\tau)^2] (w_e > 0) \quad (13)$$

To avoid the dominance of terms with different scales, normalization is performed:

$$R_{\text{normalized}} = - \left[\frac{e(t)^2}{e_{\max}^2} + \frac{(\int ed\tau)^2}{(\int ed\tau)_{\max}^2} \right] \quad (14)$$

Here, e_{\max}^2 and $(\int ed\tau)_{\max}^2$ represent empirically defined upper limit values.

Finally, the discrete action set (Action) includes the high-level parameters output by the policy network for the single-neuron PI controller: the neuron learning rates η and the transfer coefficient K adjustment increments: $\Delta\eta_p \in \{-\Delta\eta_{ps}, 0, +\Delta\eta_{ps}\}, \Delta\eta_i \in \{-\Delta\eta_{is}, 0, +\Delta\eta_{is}\}, \Delta K \in \{-\Delta K_s, 0, +\Delta K_s\}$ where $\Delta\eta_{ps}, \Delta\eta_{is}, \Delta K_s$ represent empirically set unit variations. Given the distinct impacts of learning rates η_p, η_i within the single-neuron PI controller, the numerical ranges for each adjustment coefficient are restricted accordingly: the adjustment range for $\Delta\eta_{ps}$ is relatively large, set to $[-c, c]$, and the range for $\Delta\eta_{is}$ is set to $[-c/k, c/k], k > 1$. This prevents excessive integral action from causing oscillations, thereby enhancing sample efficiency during training and facilitating quicker convergence.

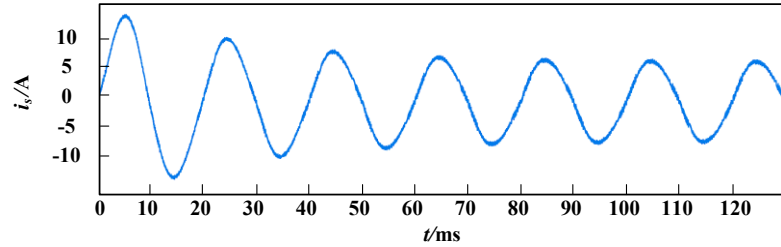
4. Simulation results and analysis

The simulation system of a single-phase VSR-PWM is established using Matlab/Simulink. Comparative analysis with the single-neuron PI control strategy is conducted to verify the correctness of the proposed control strategy. The simulation parameters for the VSR are listed in Table 1.

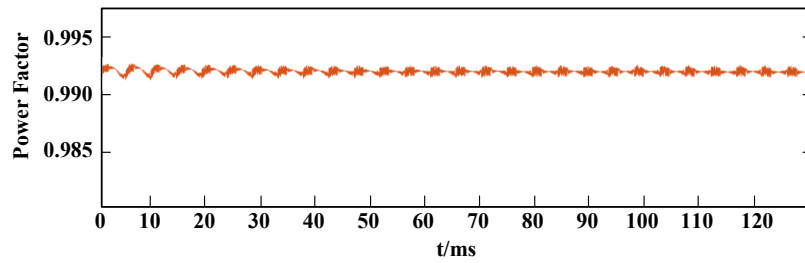
Table 1 Simulation parameters of VSR

Parameter	Value
Grid-side voltage amplitude u_{sm}/V	220
DC-side output voltage u_o/V	330
Switching frequency f_s/kHz	20
Grid-side inductance L/mH	2
DC-side capacitance $C/\mu F$	2000
Grid-side equivalent resistance R_s/Ω	0.5

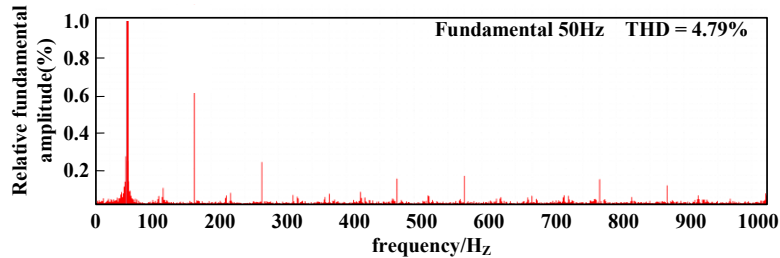
Simulation results employing the single-neuron PI control strategy are shown in Fig. 6. Under this strategy, the grid-side current waveform quality is relatively poor, displaying insufficient sinusoidal quality with a THD of 4.79% and a power factor below 0.995. By contrast, the simulation results for the proposed control method as shown in Fig. 7 achieve significantly improved performance, with a grid-side current THD of only 2.71% and high waveform quality. Furthermore, grid-side voltage and current maintain synchronized phases, achieving a power factor greater than 0.995.



(a) Grid-side current waveform

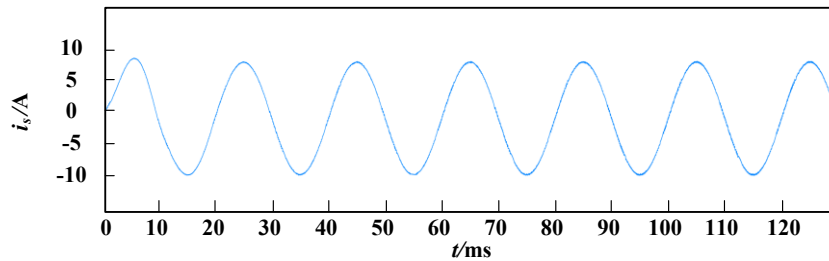


(b) Grid-side input power factor

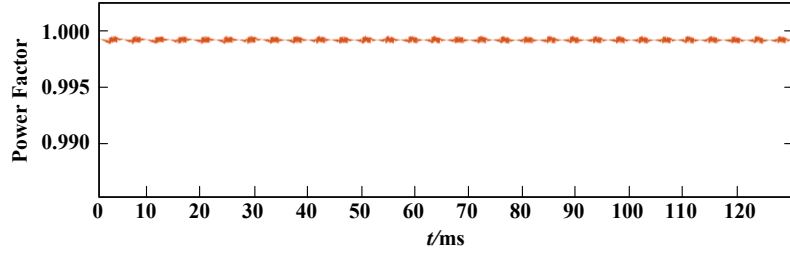


(c) Spectral analysis of grid-side current

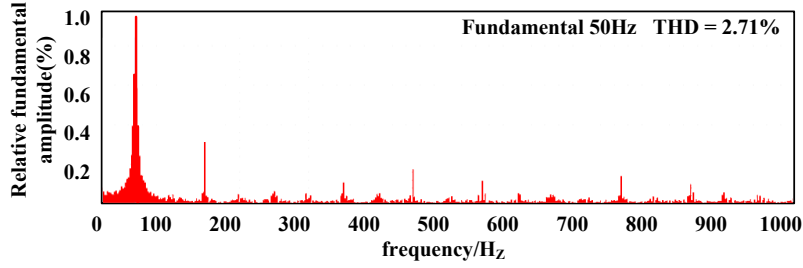
Figure 6: Simulation results of single-neuron PI control strategy



(a) Grid-side current waveform



(b) Grid-side input power factor



(c) Spectral analysis of grid-side current

Figure 7: Simulation results of the proposed control strategy

To validate the dynamic response capability of the proposed control strategy, Fig. 8 and Fig. 9 respectively illustrate simulation results for traditional and proposed control strategies under a load decrease from full load to half load. Under traditional control, u_o overshoot exceeds 35 V with a settling time around 80 ms, indicating poor dynamic performance. In contrast, with the proposed control method, u_o reaches the new steady-state within 15 ms, effectively tracking the reference voltage. The maximum voltage fluctuation throughout this process is limited to 13 V, and the grid-side current rapidly stabilizes.

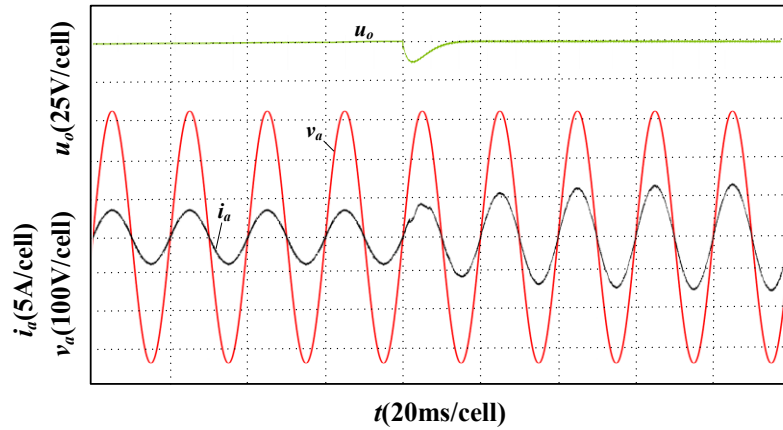


Figure 8: Simulation results of full load decrease to half load under the proposed control strategy

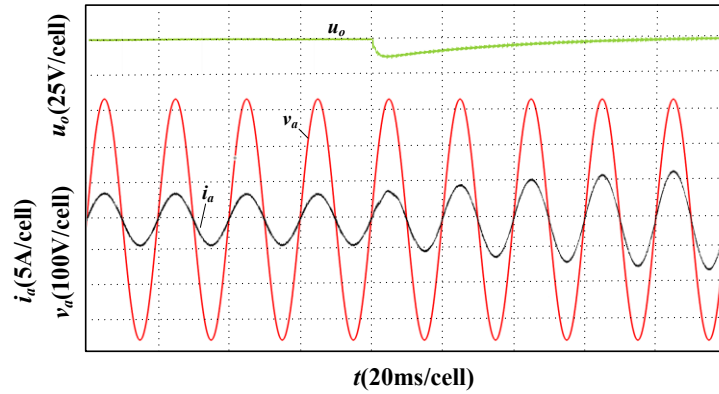


Figure 9: Simulation results of full load decrease to half load under traditional control strategy

5. Conclusion

This paper analyzed the mathematical model of a single-phase VSR-PWM in the d - q rotating coordinate system. In the loop design, incorporating a reinforcement learning-based upper-layer algorithm in the outer loop significantly improved the dynamic response of the single-neuron PI controller, effectively mitigating DC-side voltage overshoot during sudden load variations. Furthermore, the grid-side current THD and system power factor were improved. Through comparative analysis with simulations using the single-neuron PI control strategy, the following conclusions are drawn:

(1) The proposed outer-loop control strategy enhances system performance, achieving stable operation at unity power factor under varying load conditions. Both current THD and power factor are significantly improved, demonstrating that the enhanced outer-loop controller contributes to more stable inner-loop current control.

(2) The improved outer-loop controller effectively enhances the system's dynamic response. When abrupt load changes occur, either increasing or decreasing, the DC-side output voltage rapidly recovers to the set reference value, effectively suppressing voltage overshoot.

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